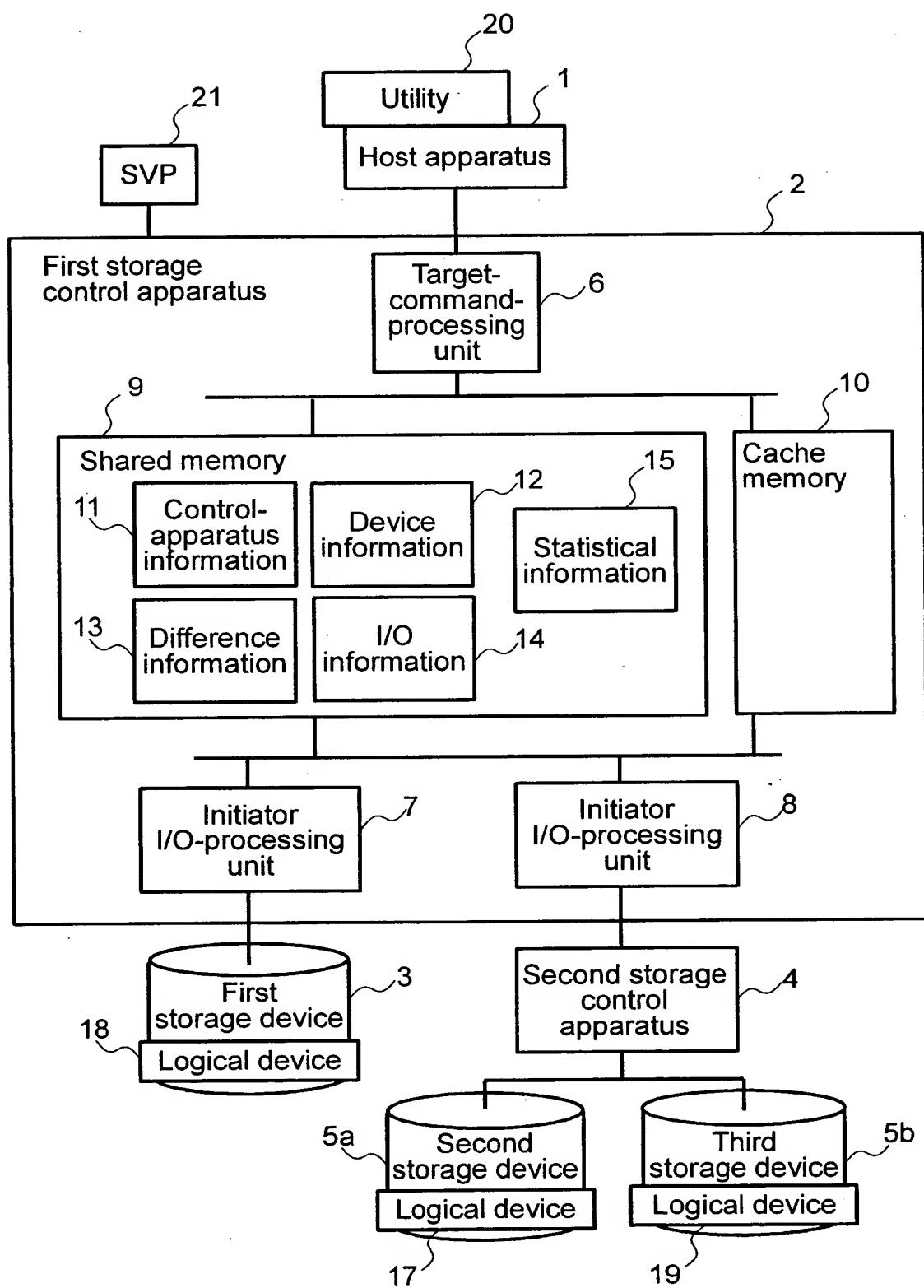


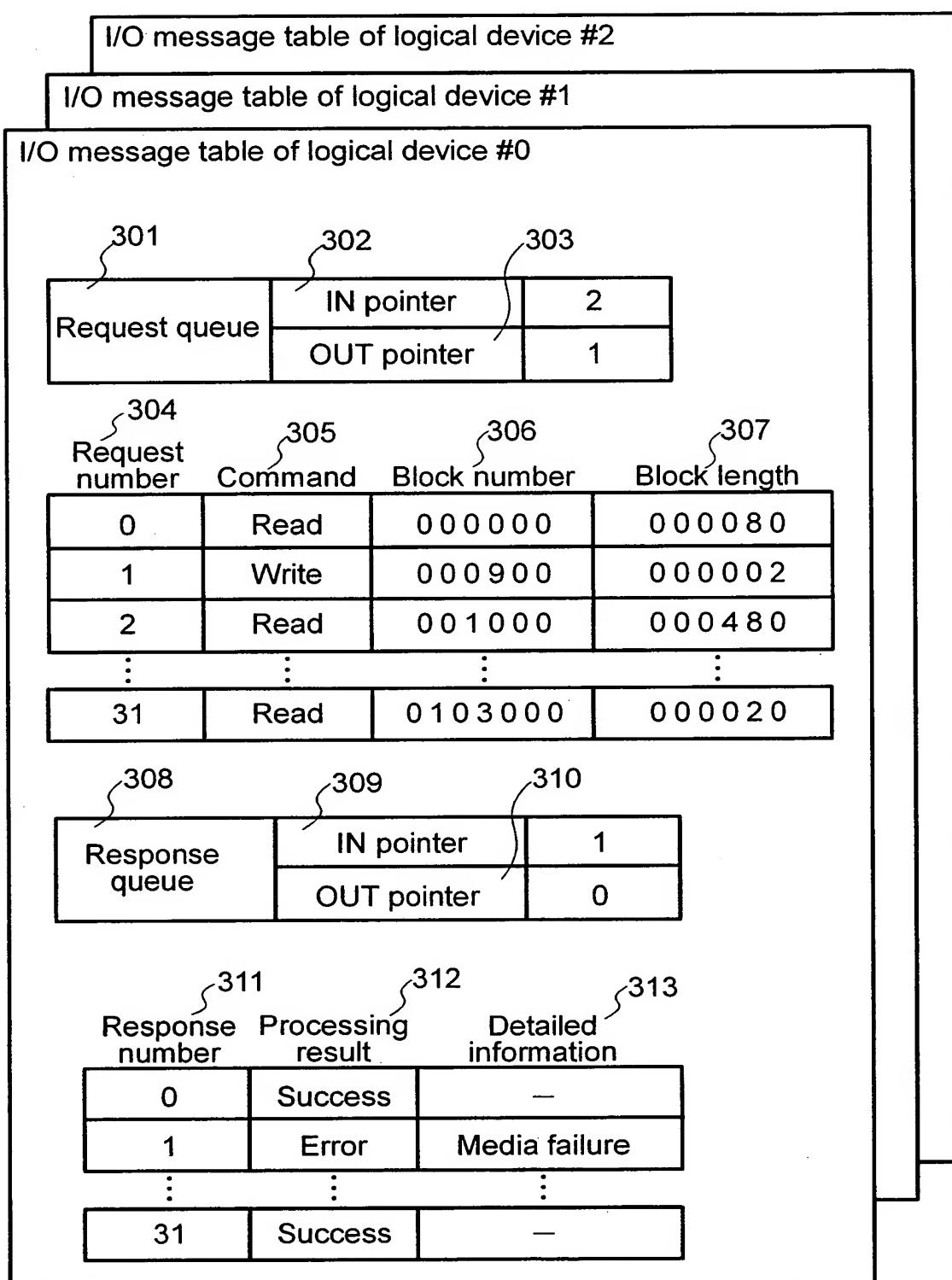
FIG.1



**FIG.2**

**FIG.3**

## FIG.4



## FIG.5

0 1 2 3 4 5 6 7 . . .

0 0 1 0 1 1 1 0 . . .

1 1 1 1 1 1 0 0 . . .

.

.

0 0 0 0 1 0 0 0 . . .

Pair number 0

0 0 0 0 0 0 0 0 . . .

0 0 0 0 0 0 0 0 . . .

.

.

0 0 0 0 0 0 0 0 . . .

Pair number 1

.

.

.

0 0 0 0 0 0 0 0 . . .

0 0 0 0 0 0 1 1 . . .

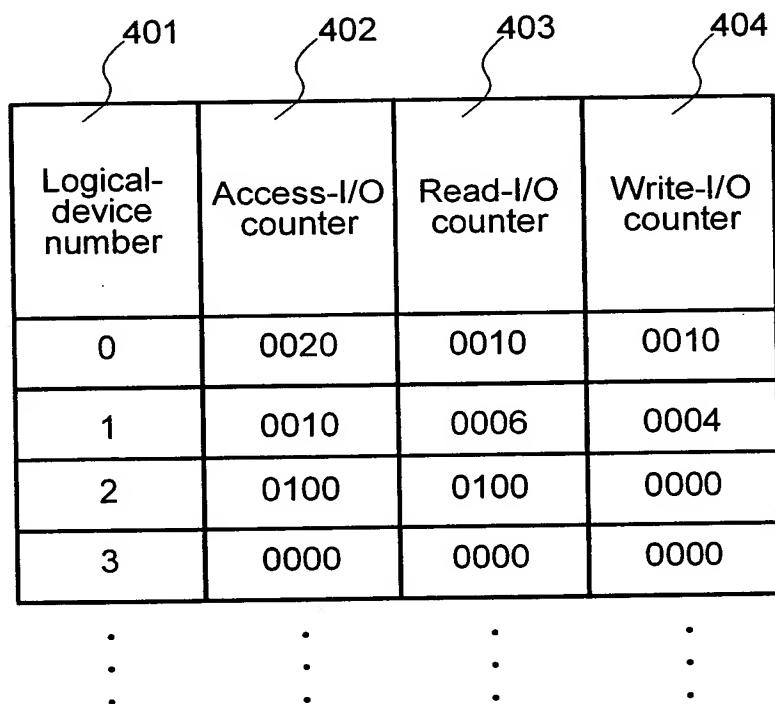
.

.

1 1 1 1 0 0 0 0 . . .

Pair number N

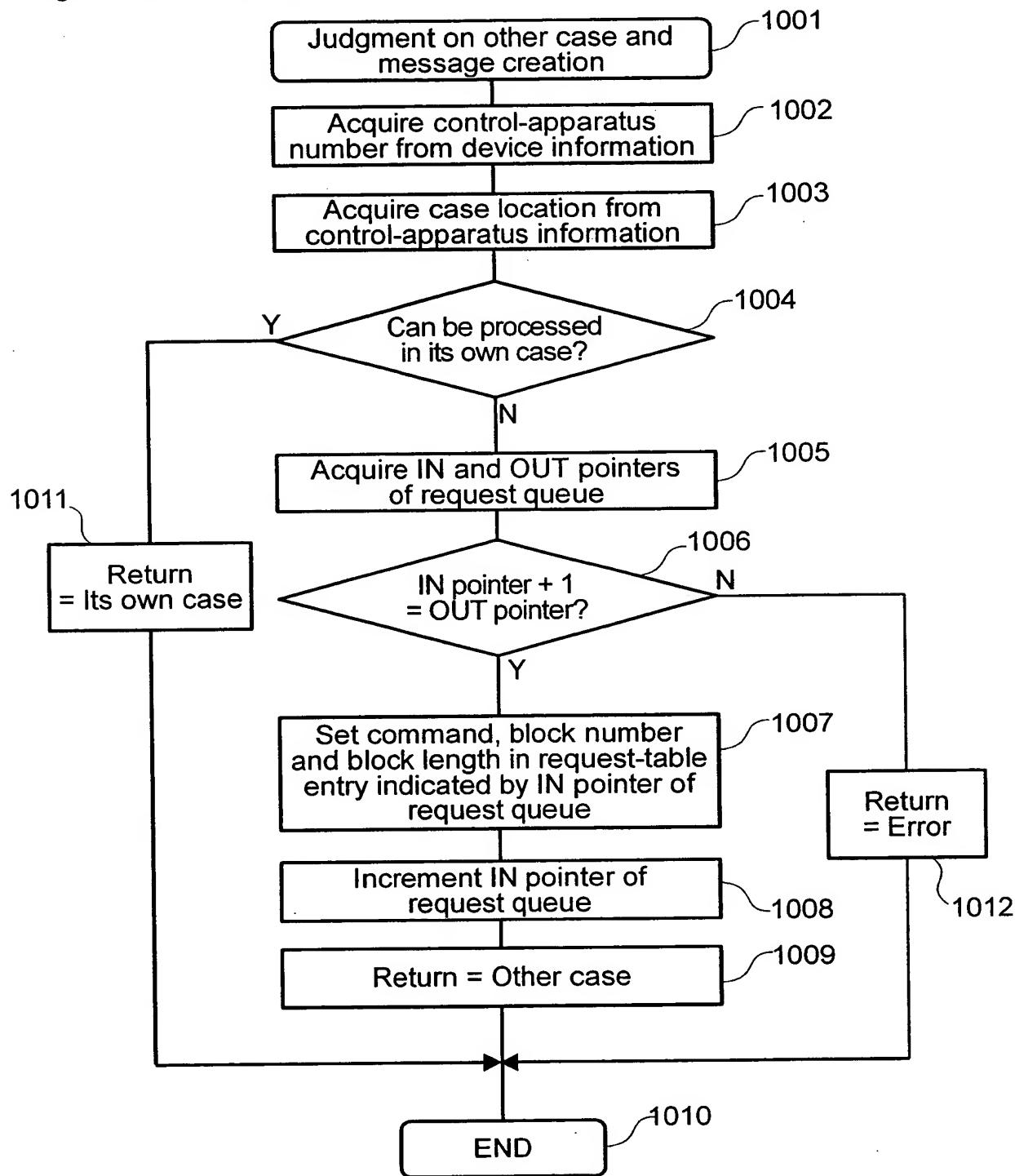
## FIG.6



Logical-device number	Access-I/O counter	Read-I/O counter	Write-I/O counter
0	0020	0010	0010
1	0010	0006	0004
2	0100	0100	0000
3	0000	0000	0000
...	...	...	...

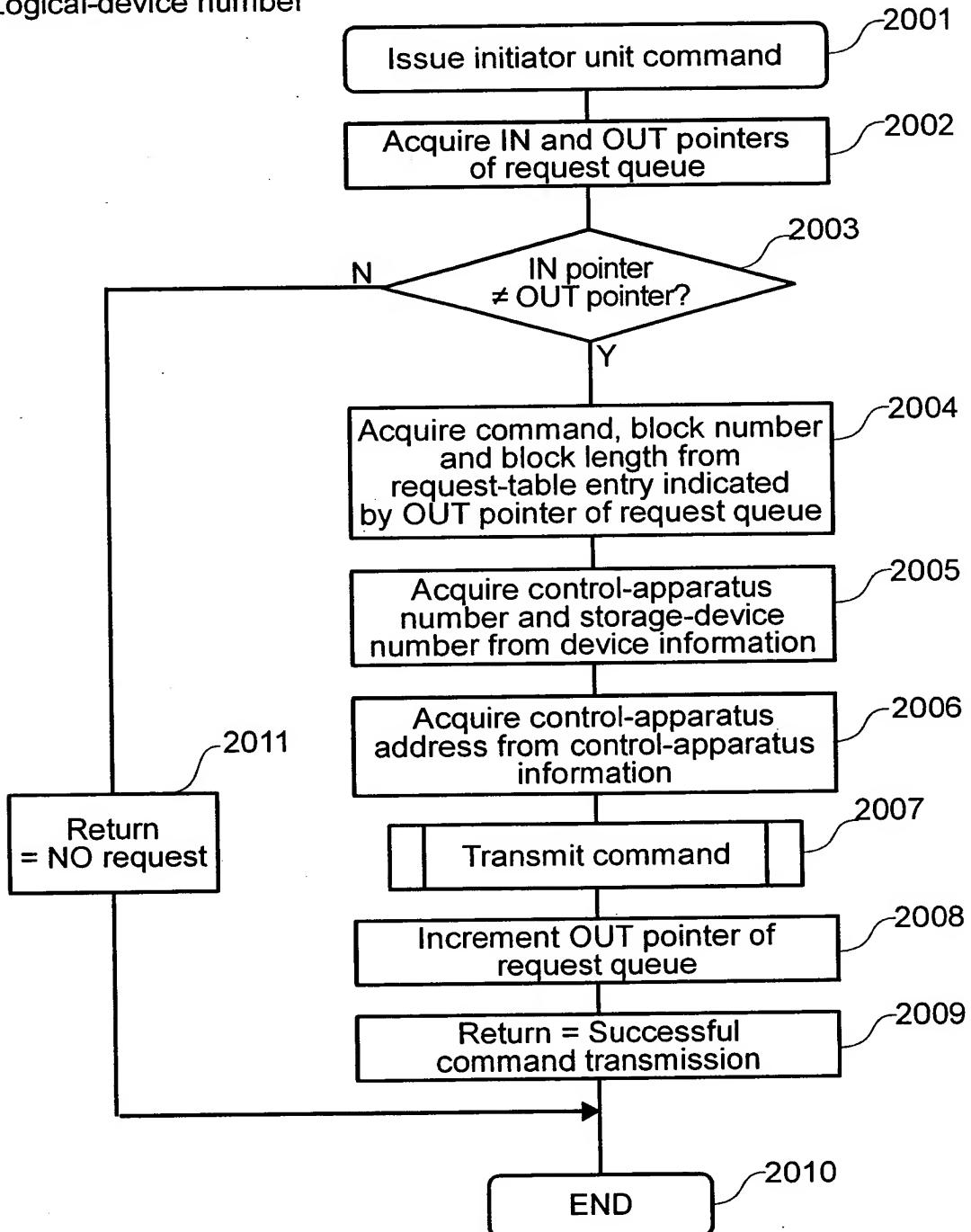
## FIG.7

Inputs:  
Logical-device number



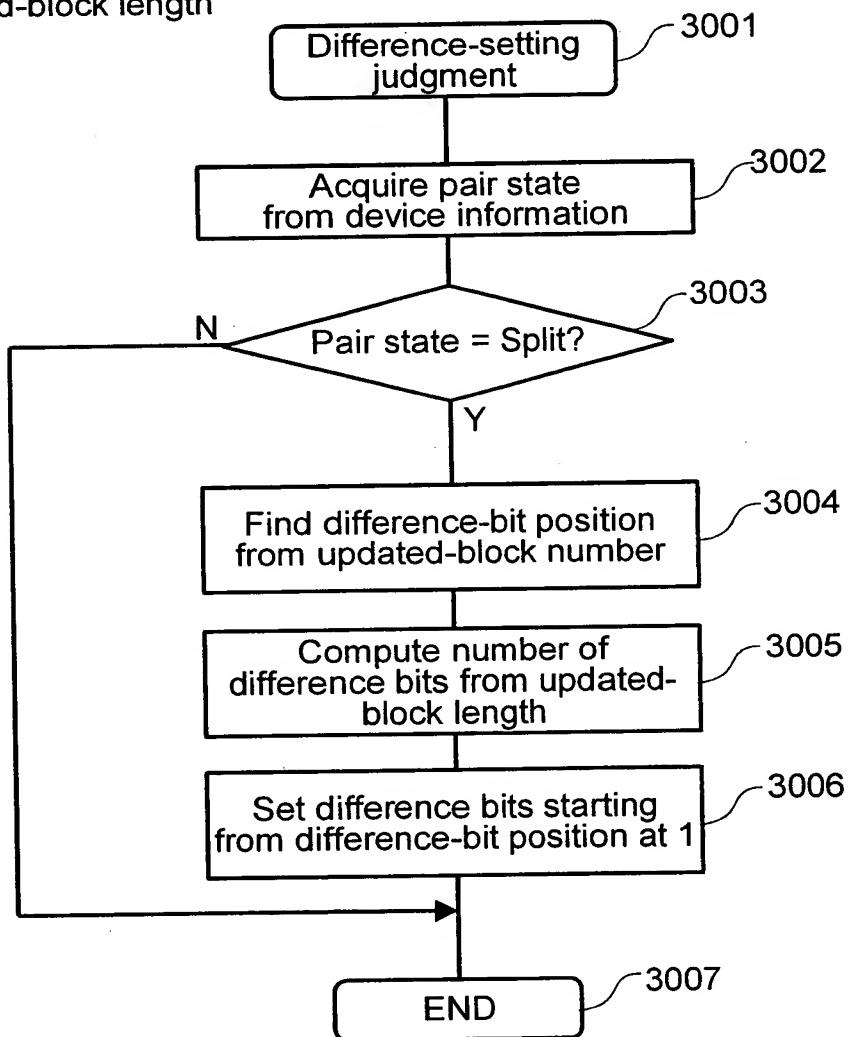
## FIG.8

Inputs:  
Logical-device number



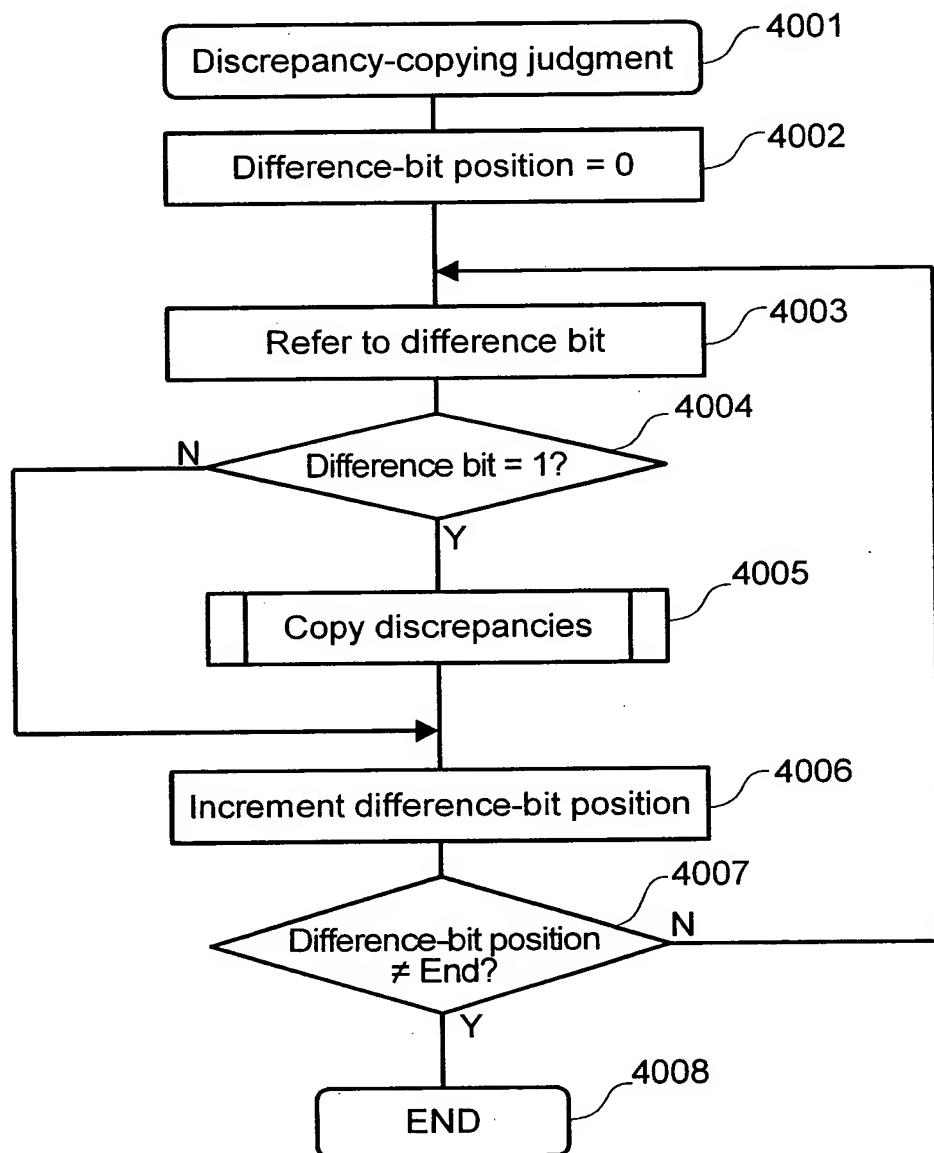
## FIG.9

Inputs:  
Logical-device number,  
updated-block number  
and updated-block length



## FIG.10

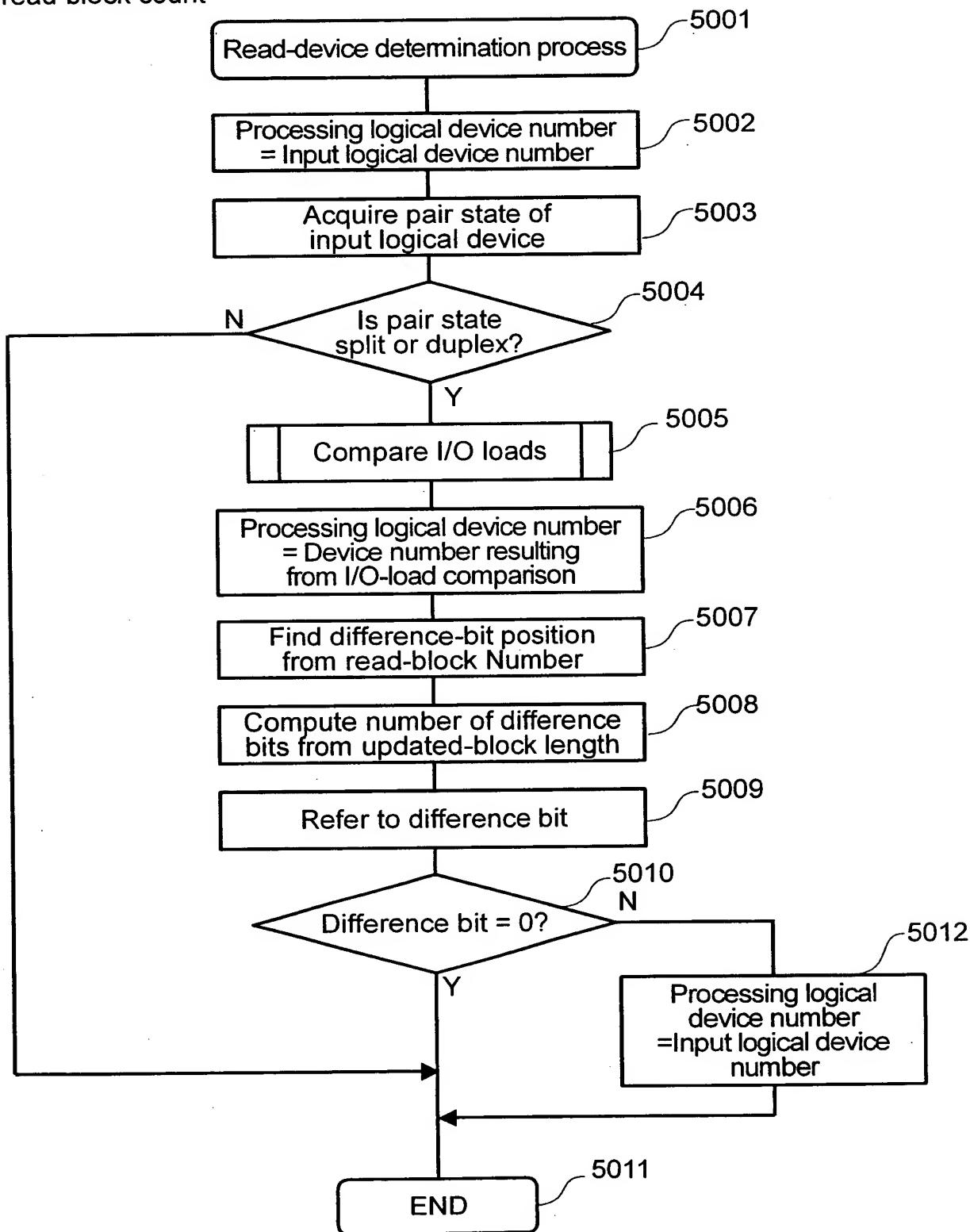
Inputs:  
Logical-device number



## FIG.11

Inputs:

Logical-device number,  
read-block number and  
read-block count



## FIG.12

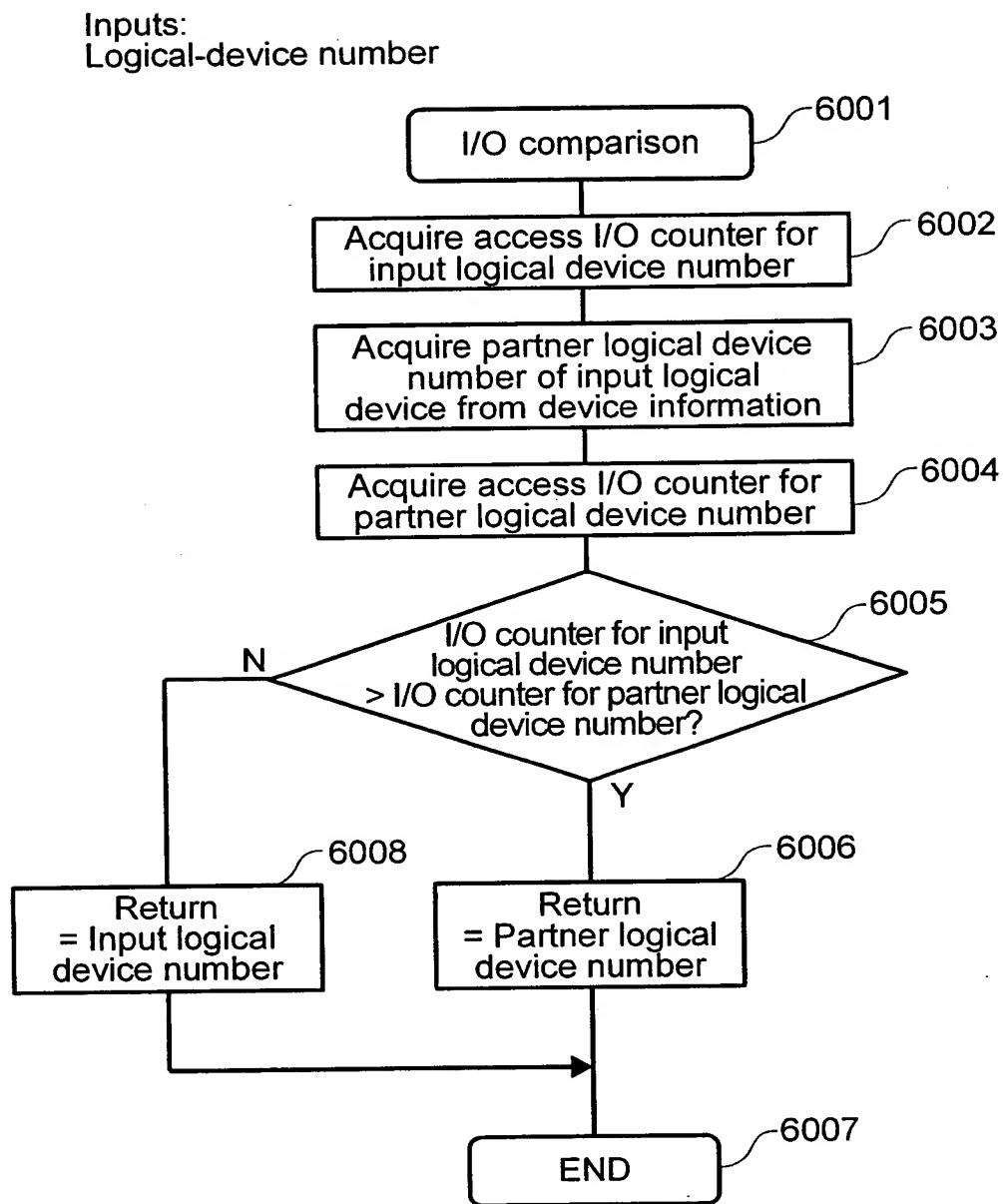
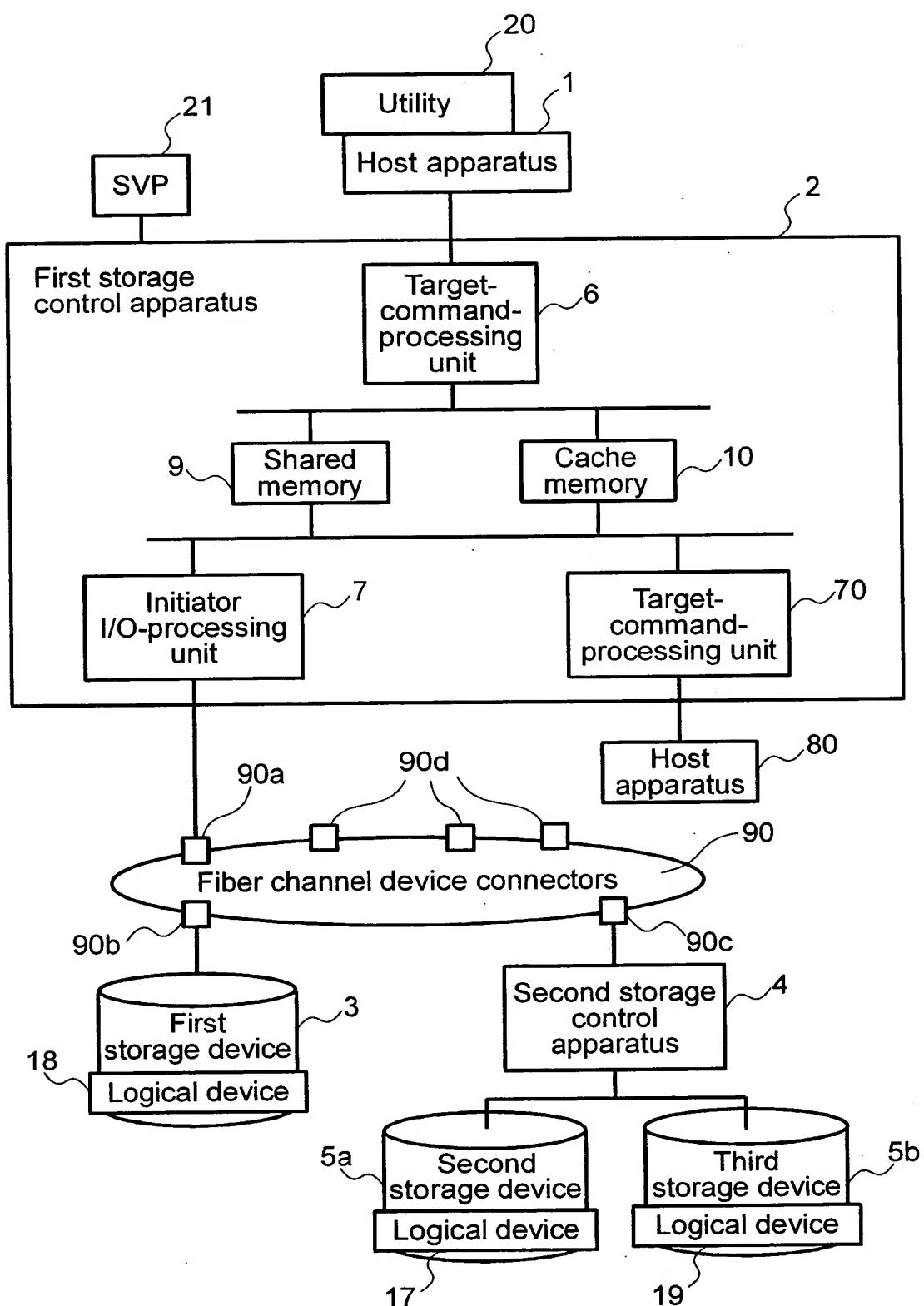


FIG.13



## FIG.14

Control-apparatus number	Case location	Initiator control unit number	Node name	Address
0	Self	0	HTC	0000E8
1	Other	0	SBC	0000E4
2	Not applicable	Not applicable	Not applicable	Not applicable
...	...	...	...	...
...	...	...	...	...
...	...	...	...	...

FIG.15

